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- Matched Instruction Set Processors (MISPs) in digital communication SOC's.
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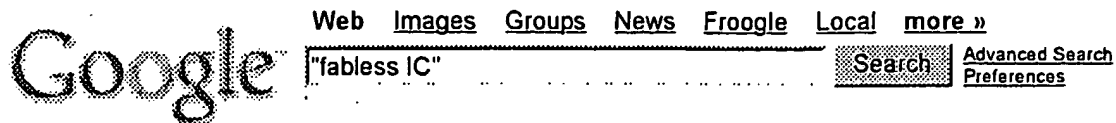
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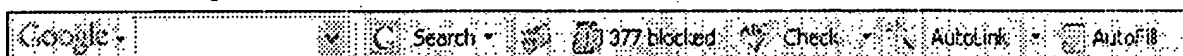
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